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Terms	Documents
L19 and (confirm\$ or determin\$ or check\$ or identi\$) same (upgrad\$ or updat\$ or modi\$ or reprogram\$)	1

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L38



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side by side			
<i>DB=USPT; PLUR=YES; OP=ADJ</i>			
<u>L38</u>	l19 and (confirm\$ or determin\$ or check\$ or identi\$) same (upgrad\$ or updat\$ or modi\$ or reprogram\$)	1	<u>L38</u>
<u>L37</u>	L36 and re\$	1	<u>L37</u>
<u>L36</u>	l19 and (initial\$ or start\$ or boot\$ or signal\$)	1	<u>L36</u>
<u>L35</u>	l19 and (shift\$ or switch\$ or on\$ or off\$)	1	<u>L35</u>
<u>L34</u>	l19 and (AC or alternat\$ or power\$)	1	<u>L34</u>
<u>L33</u>	l32 and (power\$ near9 fail\$)	1	<u>L33</u>
<u>L32</u>	l19 and (remov\$ or delet\$ or power\$ or off\$)	1	<u>L32</u>
<u>L31</u>	l19 and user\$	1	<u>L31</u>
<u>L30</u>	l19 and (blank\$ or eras\$ or delet\$ or remov\$)	1	<u>L30</u>
<u>L29</u>	l19 and (updat\$ or upgrad\$ or modif\$ Or alter or chang\$ or reprogram\$)	1	<u>L29</u>
<u>L28</u>	l19 and (cop\$ or load\$ Or download\$) near9 ram\$	1	<u>L28</u>

<u>L27</u>	l19 and (determin\$ or select\$ or check\$) same control\$	1	<u>L27</u>
<u>L26</u>	l19 and (reprogram\$ or delet\$ or replac\$ or updat\$ Or upgrad\$)	1	<u>L26</u>
<u>L25</u>	l19 and (processor\$ or control\$ or micro\$).	1	<u>L25</u>
<u>L24</u>	l19 and post\$	1	<u>L24</u>
<u>L23</u>	l19 and (recover\$)	1	<u>L23</u>
<u>L22</u>	l19 and (updat\$ or upgrad\$ or modif\$ Or alter or chang\$ or reprogram\$) near4 (ROM or BIOS)	1	<u>L22</u>
<u>L21</u>	l19 and (updat\$ or upgrad\$ or modif\$ Or alter or chang\$) near4 (ROM or BIOS)	1	<u>L21</u>
<u>L20</u>	L19 and first memory and second memory	0	<u>L20</u>
<u>L19</u>	6442067.pn.	1	<u>L19</u>
<u>L18</u>	L17 and (updat\$ or modif\$ or upgrad\$ or chang\$ or alter) near5 (memory\$ or bios\$ or flash\$ or Rom\$)	67	<u>L18</u>
<u>L17</u>	L13 and l9	109	<u>L17</u>
<u>L16</u>	L13 and l10	0	<u>L16</u>
<u>L15</u>	L13 and l11	0	<u>L15</u>
<u>L14</u>	L13 and l12	0	<u>L14</u>
<u>L13</u>	717/168-178.ccls.	1188	<u>L13</u>
<u>L12</u>	L11 and (updat\$ Or modif\$ Or upgrad\$) near5 (memory\$ Or bios\$ or flash\$ or rom\$)	39	<u>L12</u>
<u>L11</u>	L10 and ((power on self\$) or post\$)	46	<u>L11</u>
<u>L10</u>	L9 and (rom\$ near5 (two\$ near4 (block\$ Or div\$ or separat\$)))	168	<u>L10</u>
<u>L9</u>	L8 and rom	20464	<u>L9</u>
<u>L8</u>	(first near4 memory\$) and (second\$ near4 memory\$)	68239	<u>L8</u>
<u>L7</u>	L6 and (STB or test\$)	1	<u>L7</u>
<u>L6</u>	l4 and power\$	1	<u>L6</u>
<u>L5</u>	l4 and post\$	0	<u>L5</u>
<u>L4</u>	5960445.pn.	1	<u>L4</u>
<u>L3</u>	4960445.pn.	1	<u>L3</u>
<u>L2</u>	6984590.pn.	1	<u>L2</u>
<u>L1</u>	5784317.pn.	1	<u>L1</u>

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(713/2).ccls.	1137

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Search:

L12

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<u>L11</u>	700/81.ccls.	120	<u>L11</u>
<u>L10</u>	L9 and l8	3	<u>L10</u>
<u>L9</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	156	<u>L9</u>
<u>L8</u>	711/102-104.ccls.	1382	<u>L8</u>
<i>DB=TDBD; PLUR=YES; OP=ADJ</i>			
<u>L7</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	0	<u>L7</u>
<i>DB=DWPI; PLUR=YES; OP=ADJ</i>			
<u>L6</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	0	<u>L6</u>
<i>DB=JPAB; PLUR=YES; OP=ADJ</i>			

<u>L5</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	0	<u>L5</u>
<i>DB=EPAB; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	0	<u>L4</u>
<i>DB=USOC; PLUR=YES; OP=ADJ</i>			
<u>L3</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	1	<u>L3</u>
<i>DB=PGPB; PLUR=YES; OP=ADJ</i>			
<u>L2</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory and POST	69	<u>L2</u>
<u>L1</u>	first memory and second memory and rom and ram and (update\$ or upgrad\$ or modif\$) near4 memory	503	<u>L1</u>

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1 [Accelerating shared virtual memory via general-purpose network interface support](#)



Angelos Bilas, Dongming Jiang, Jaswinder Pal Singh

 February 2001 **ACM Transactions on Computer Systems (TOCS)**, Volume 19 Issue 1

Publisher: ACM Press

 Full text available: [pdf\(178.88 KB\)](#)

 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)

Clusters of symmetric multiprocessors (SMPs) are important platforms for high-performance computing. With the success of hardware cache-coherent distributed shared memory (DSM), a lot of effort has also been made to support the coherent shared-address-space programming model in software on clusters. Much research has been done in fast communication on clusters and in protocols for supporting software shared memory across them. However, the performance of software virtual memory (SVM) is sti ...

Keywords: applications, clusters, shared virtual memory, system area networks

2 [Cellular disco: resource management using virtual clusters on shared-memory multiprocessors](#)



Kinshuk Govil, Dan Teodosiu, Yongqiang Huang, Mendel Rosenblum

 August 2000 **ACM Transactions on Computer Systems (TOCS)**, Volume 18 Issue 3

Publisher: ACM Press

 Full text available: [pdf\(287.05 KB\)](#)

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Despite the fact that large-scale shared-memory multiprocessors have been commercially available for several years, system software that fully utilizes all their features is still not available, mostly due to the complexity and cost of making the required changes to the operating system. A recently proposed approach, called Disco, substantially reduces this development cost by using a virtual machine monitor that leverages the existing operating system technology. In this paper we present a ...

Keywords: fault containment, resource management, scalable multiprocessors, virtual machines

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Citation & Abstract

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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Digital Object Identifier 10.1109/TC.2002.1017700
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- ☐ 3. **Self-adjusting output data compression: An efficient BIST technique for RAMs**
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